

O P E R A T I O N S

MAY 04 2005

U.S. PATENT & TRADEMARK OFFICE

TRANSMITTAL LETTER
(General - Patent Pending)

Docket No.
SETI-0002DIV

In Re Application No. Khan et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/647,714	8/25/2003	David Nelms	23550	1713	2476

Title: METAL OXIDE SEMICONDUCTOR HETEROSTRUCTURE FIELD EFFECT TRANSISTOR

COMMISSIONER FOR PATENTS:

Transmitted herewith is:

Certificate of Correction with Patent Office Error Noted

in the above identified application.

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Signature

Dated: May 2, 2005

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on May 2, 2005.

(Date)



Signature of Person Mailing Correspondence

Christine Martinec Vlahides

Typed or Printed Name of Person Mailing Correspondence

CC:

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 6,878,593

DATED : April 12, 2005

INVENTOR(S) : Khan et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 3, line 1, please insert -- O -- before the word means

Claim 1, column 8, line 57, insert -- disposed -- before the word above

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PATENT NO. 6,878,593

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